

1/20

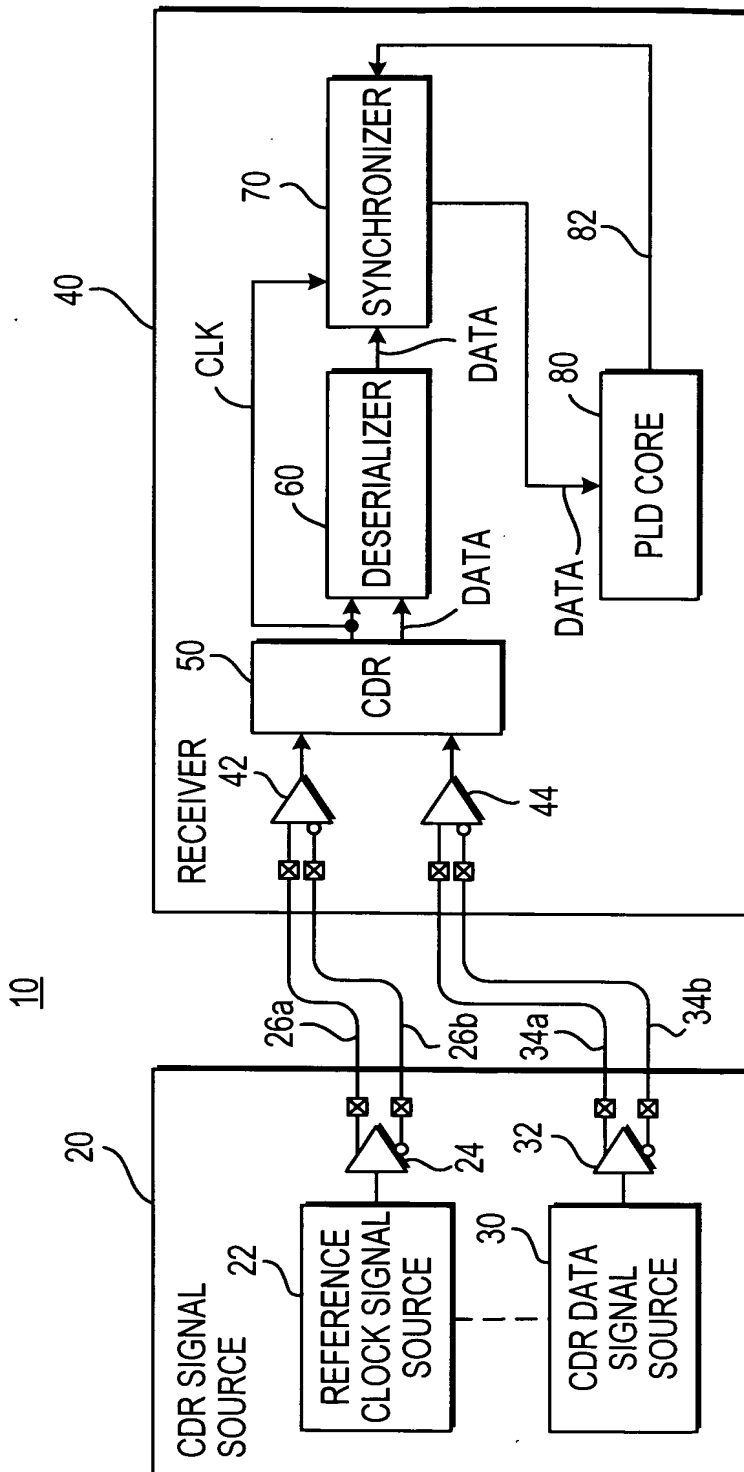


FIG. 1

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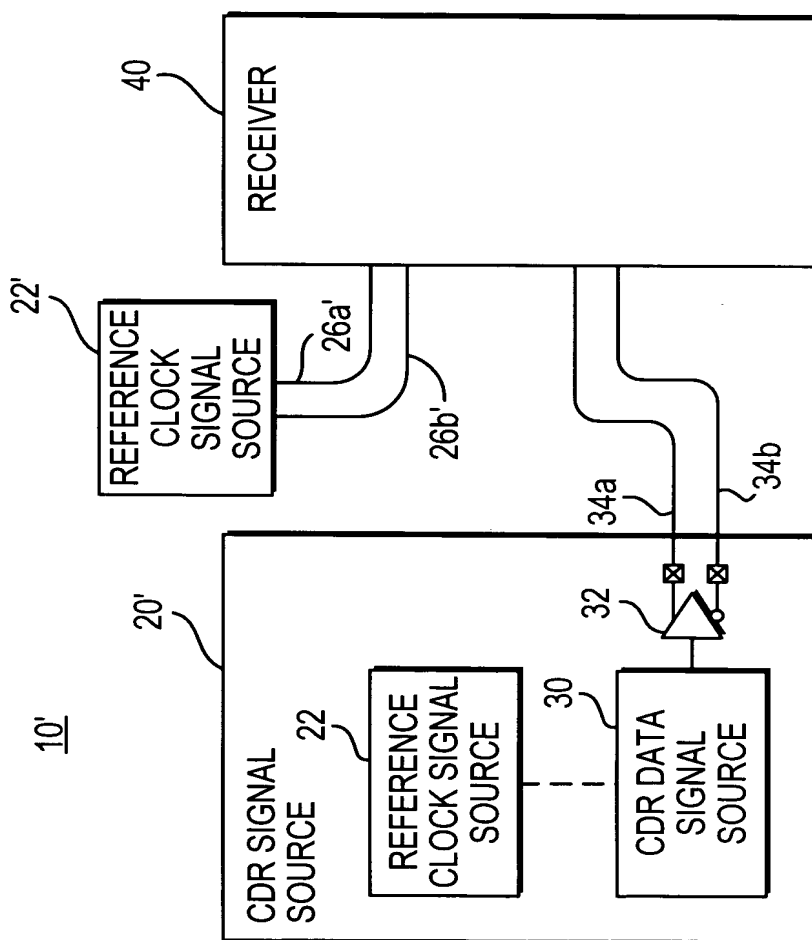


FIG. 1A

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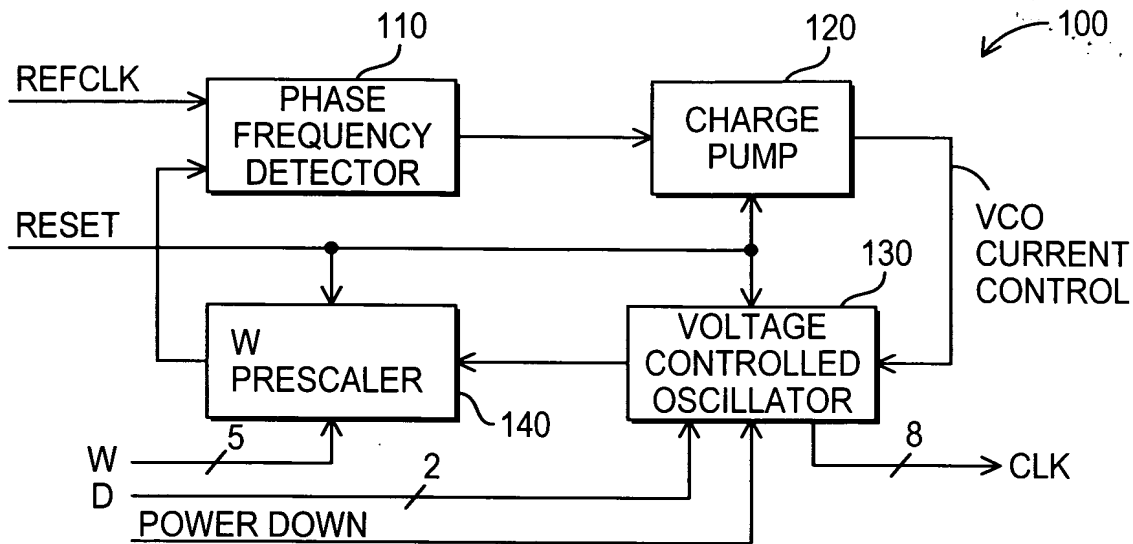


FIG. 2

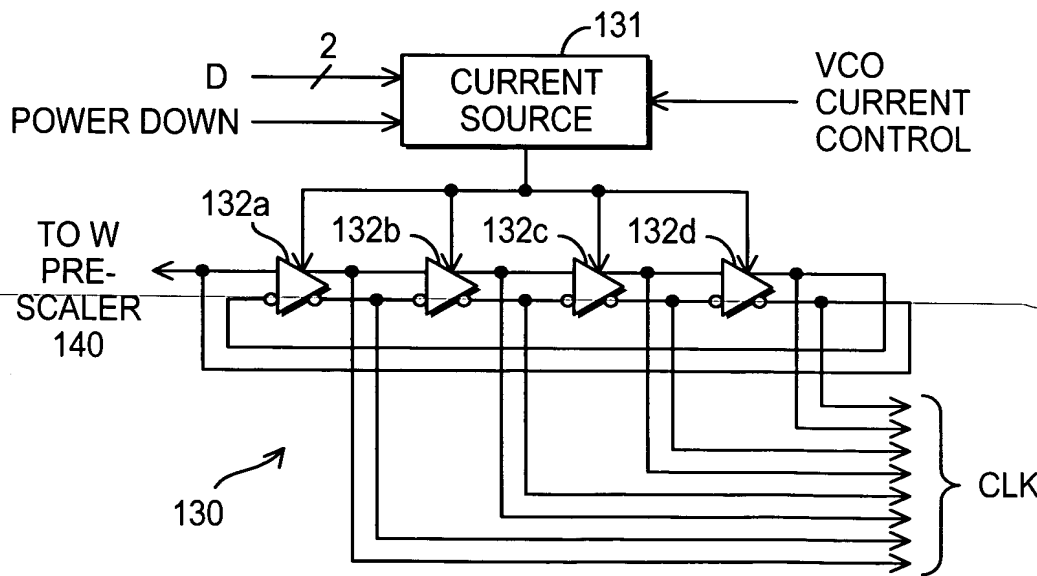


FIG. 3

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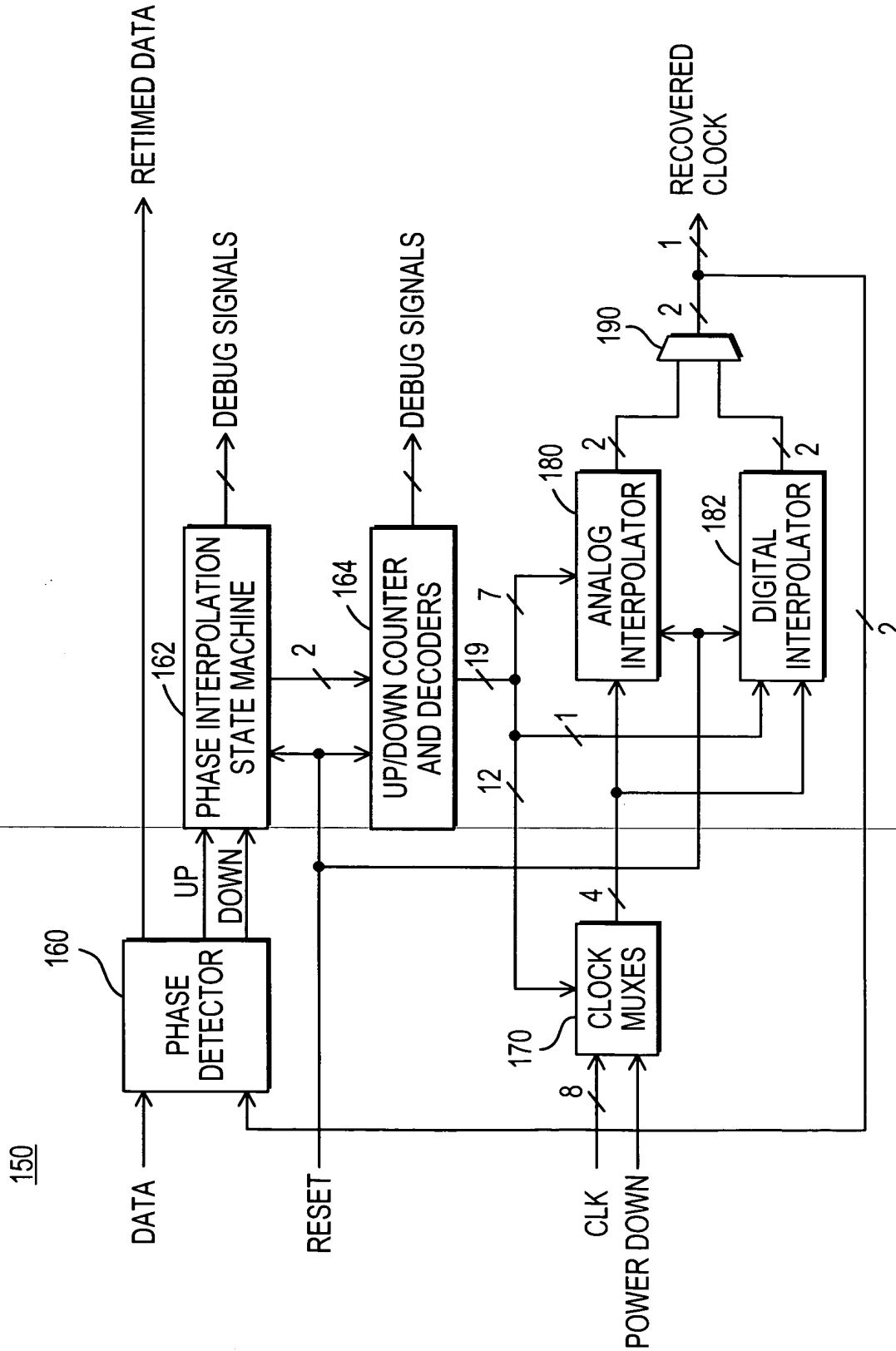


FIG. 4

FIG. 4 is a block diagram of a clock recovery system.

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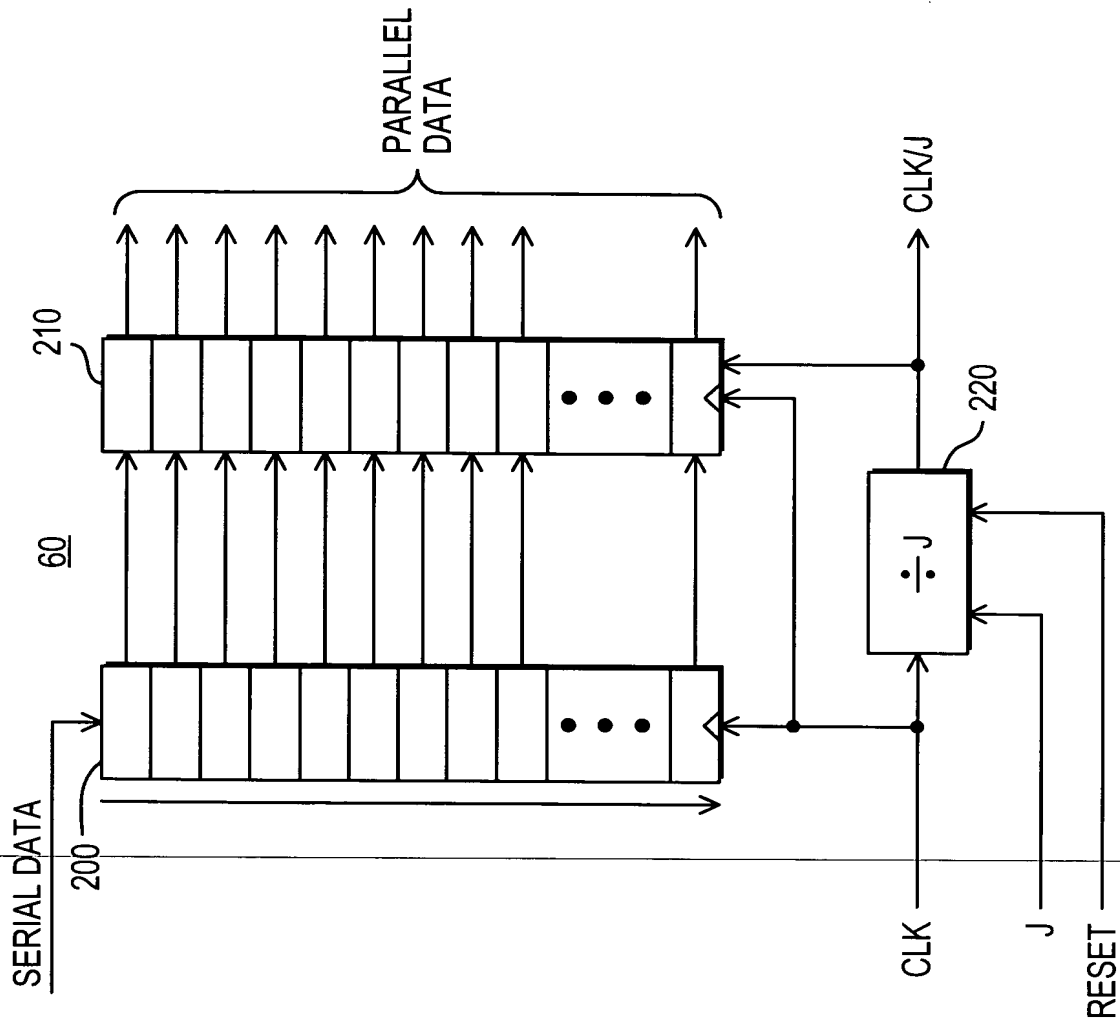


FIG. 5

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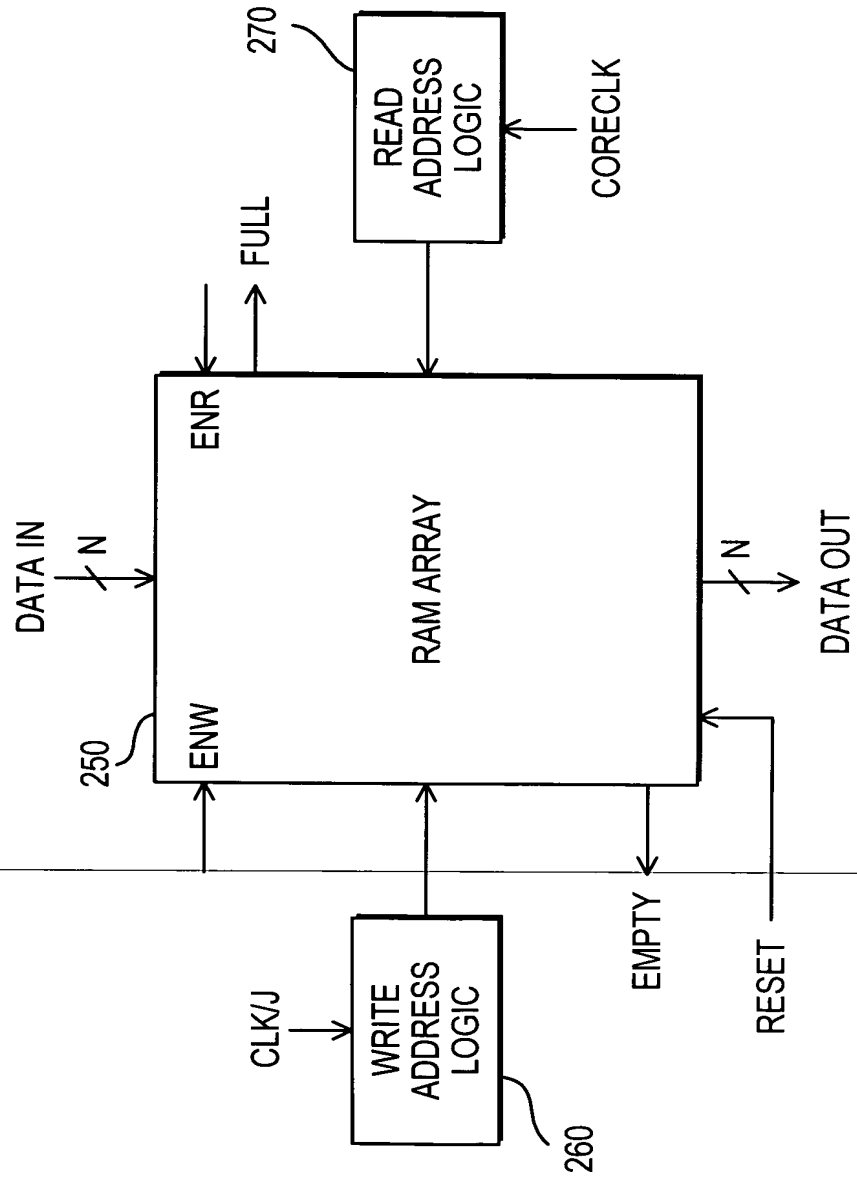


FIG. 6

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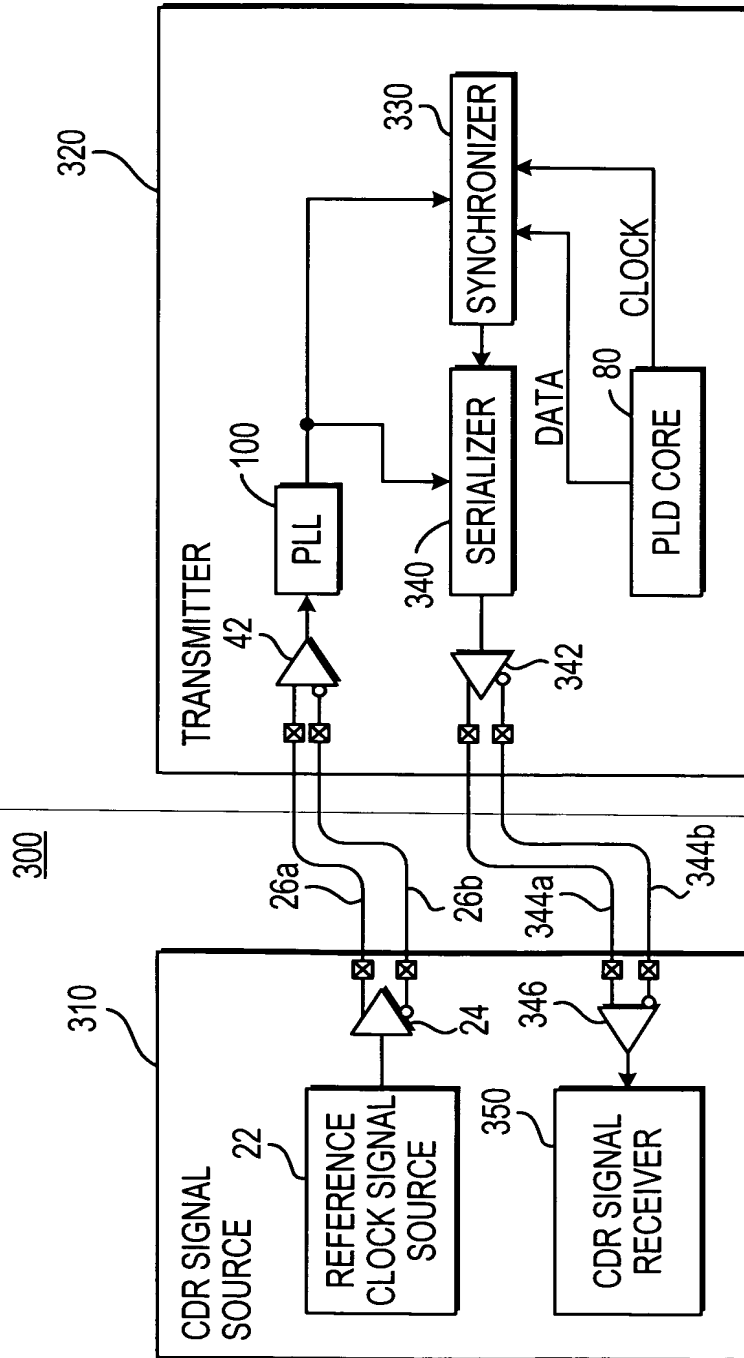


FIG. 7

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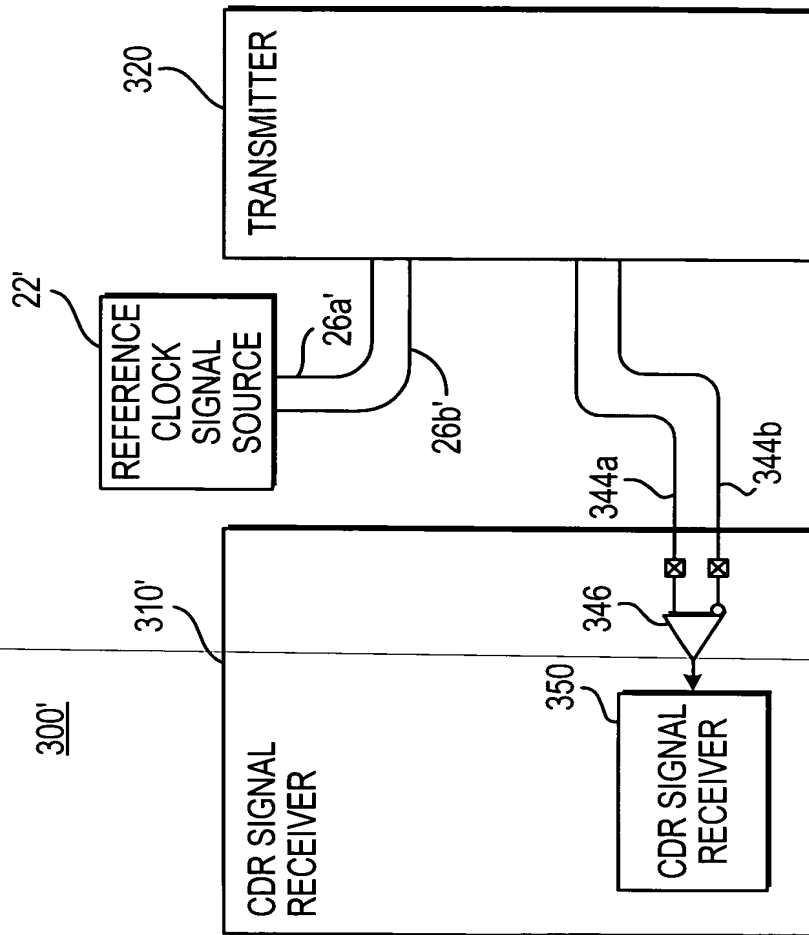


FIG. 7A



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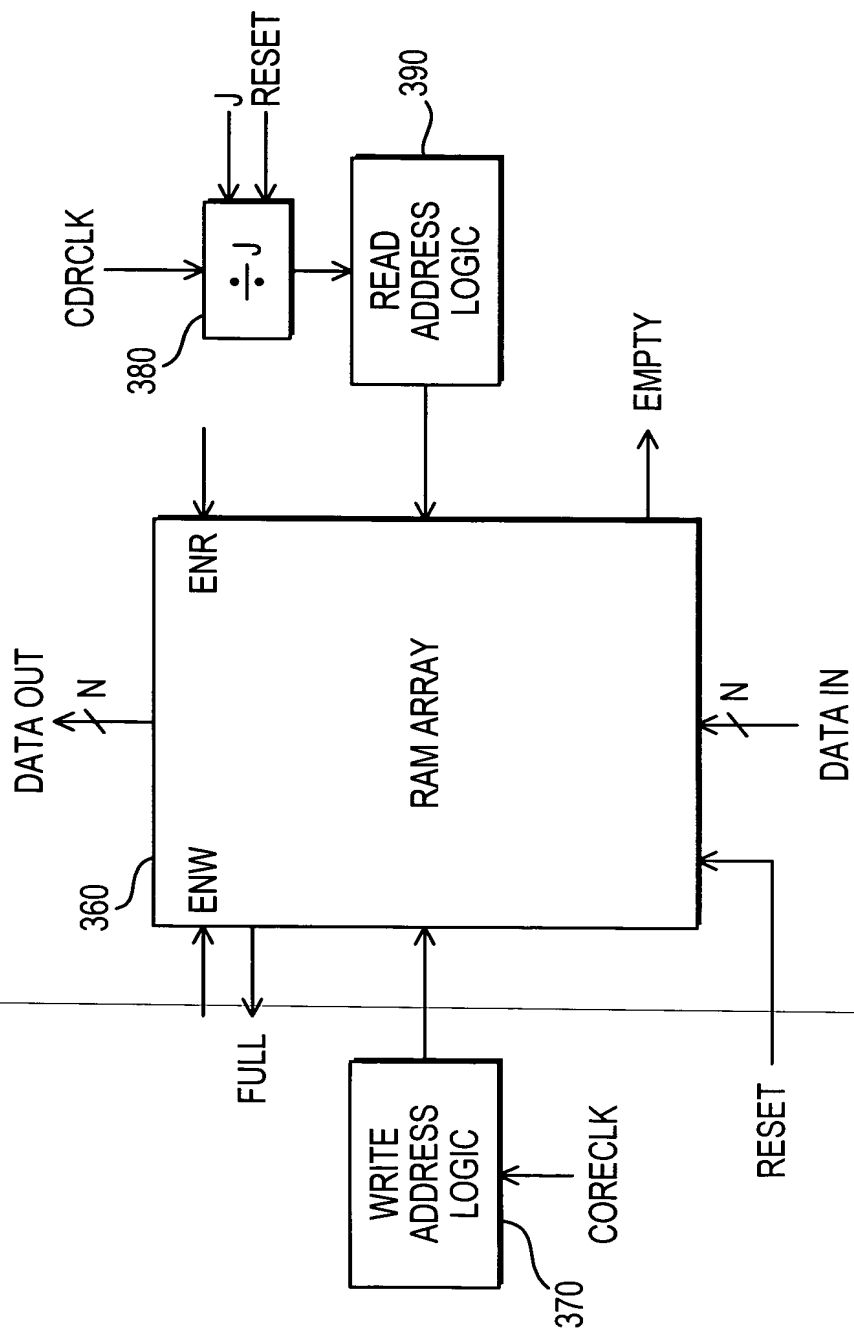


FIG. 8

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FIG. 8 of 20

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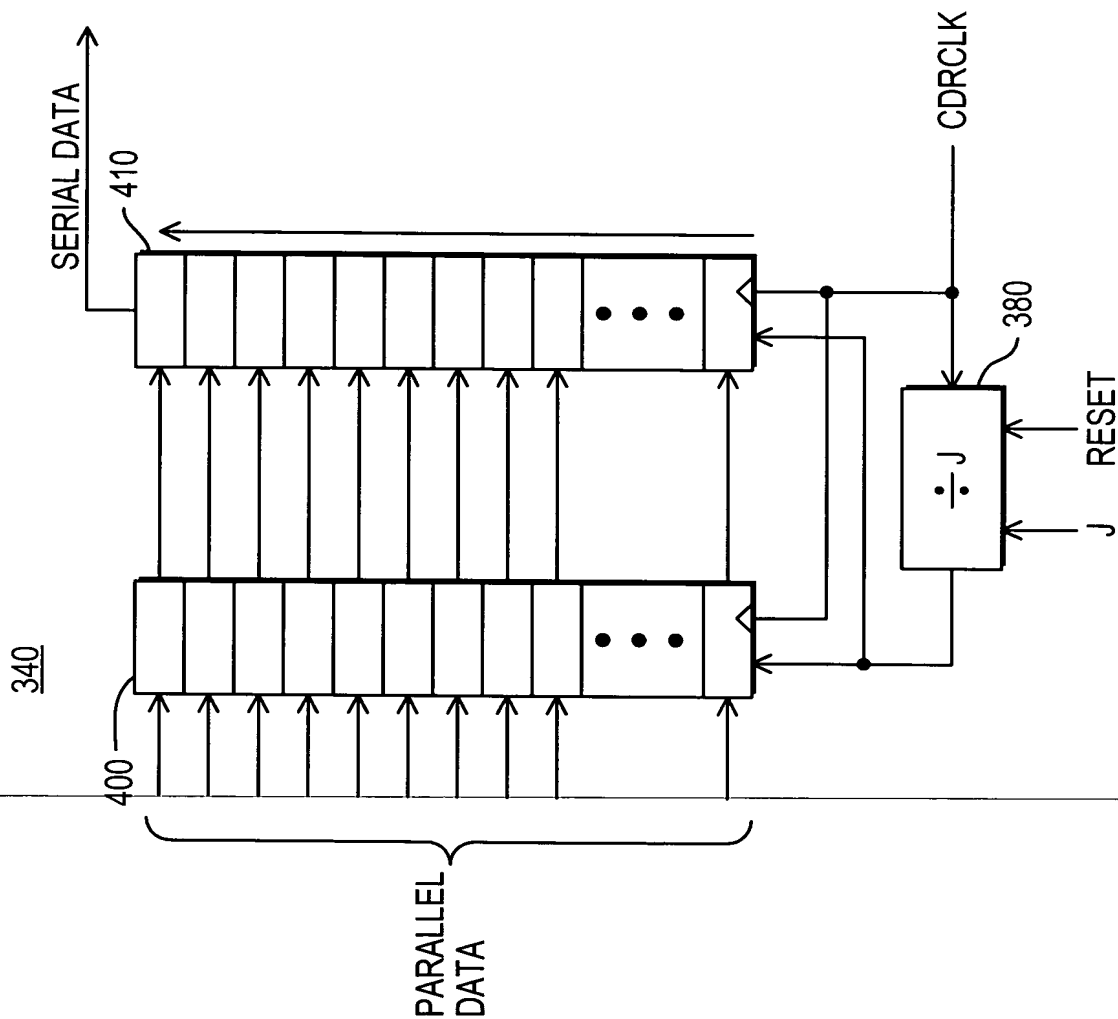


FIG. 9

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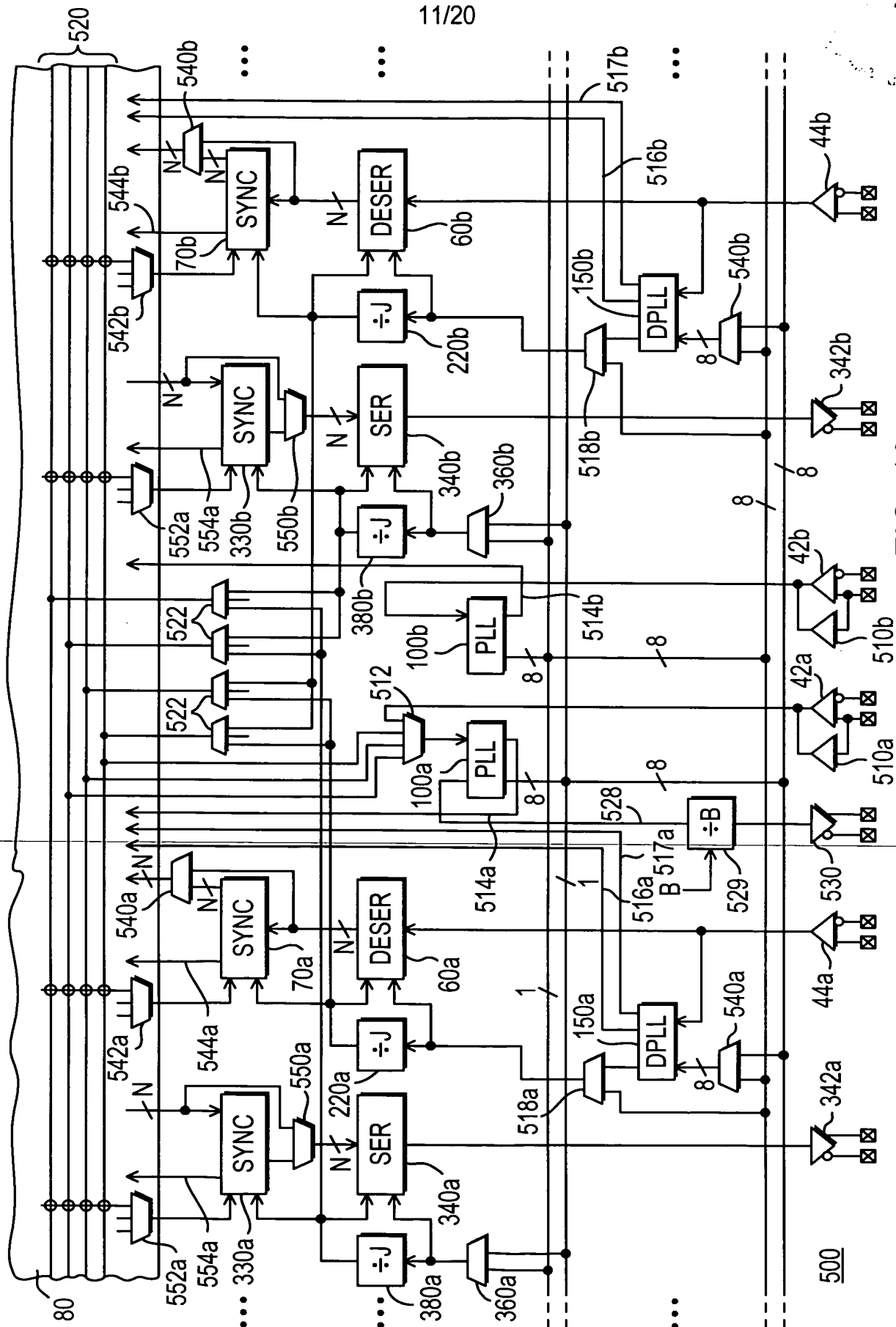


FIG. 10

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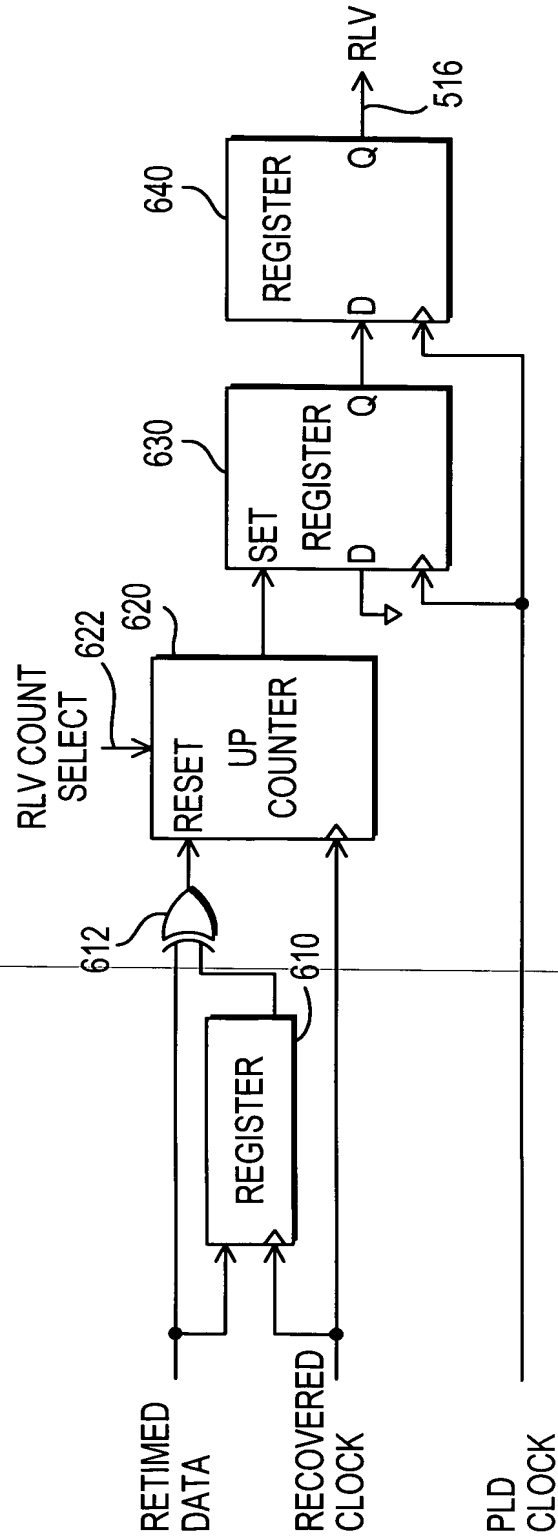


FIG. 10A

FIG. 10A is a block diagram of a circuit 600.

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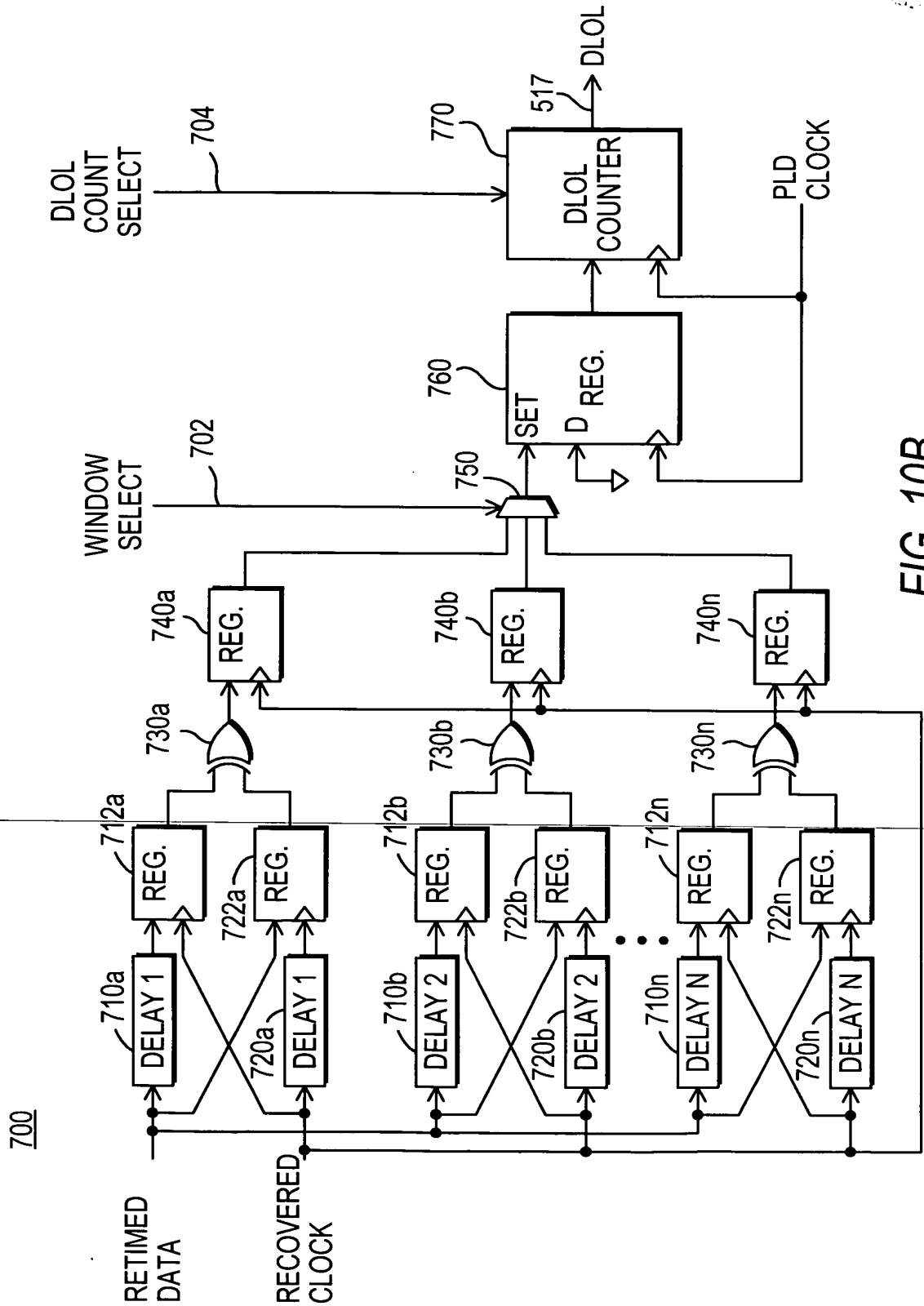


FIG. 10B

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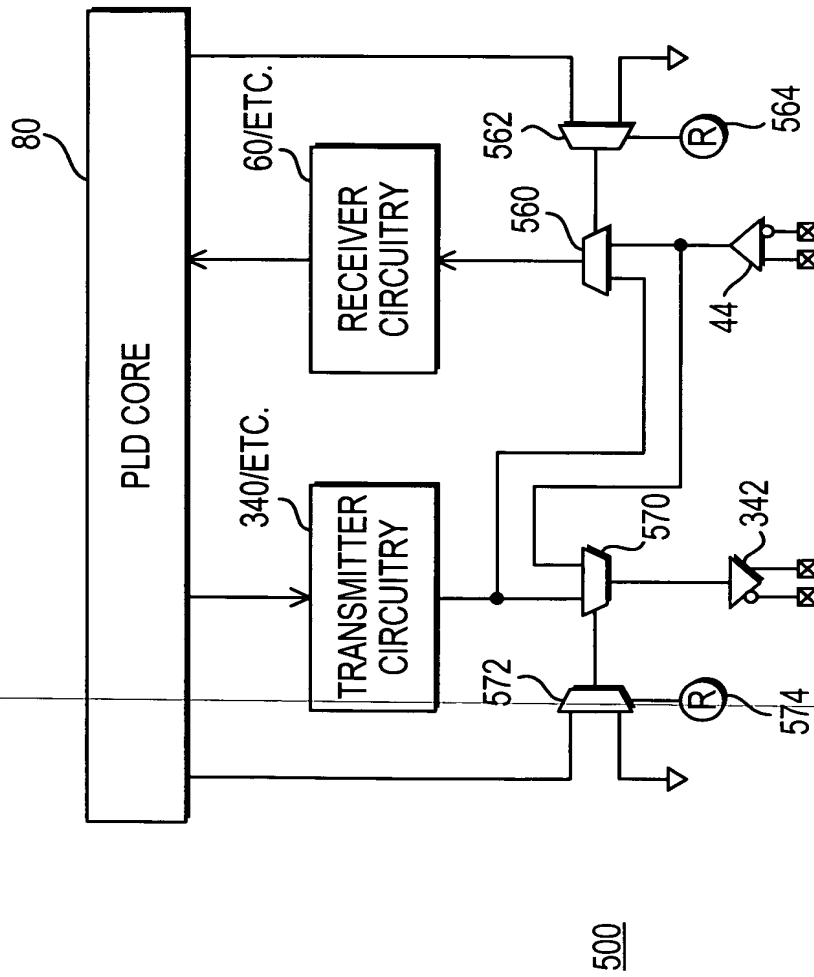
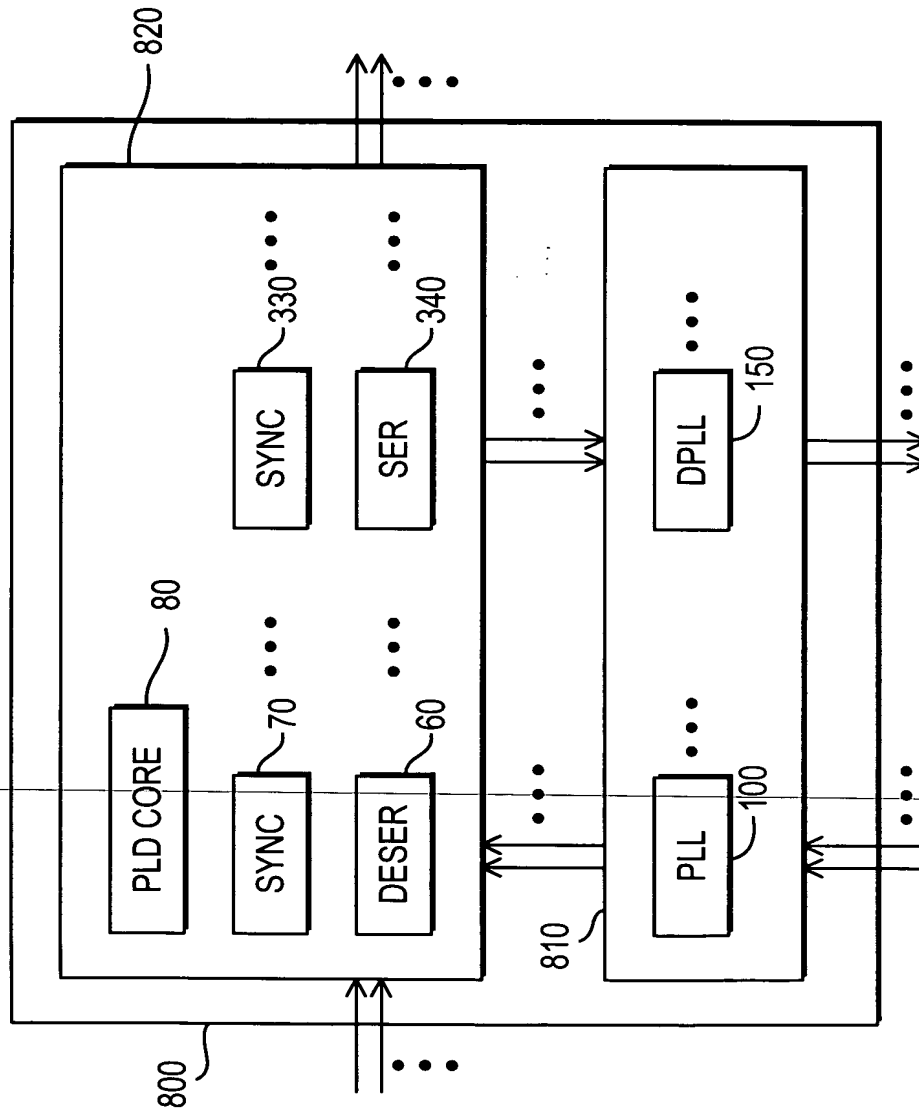


FIG. 10C



**FIG. 11A**

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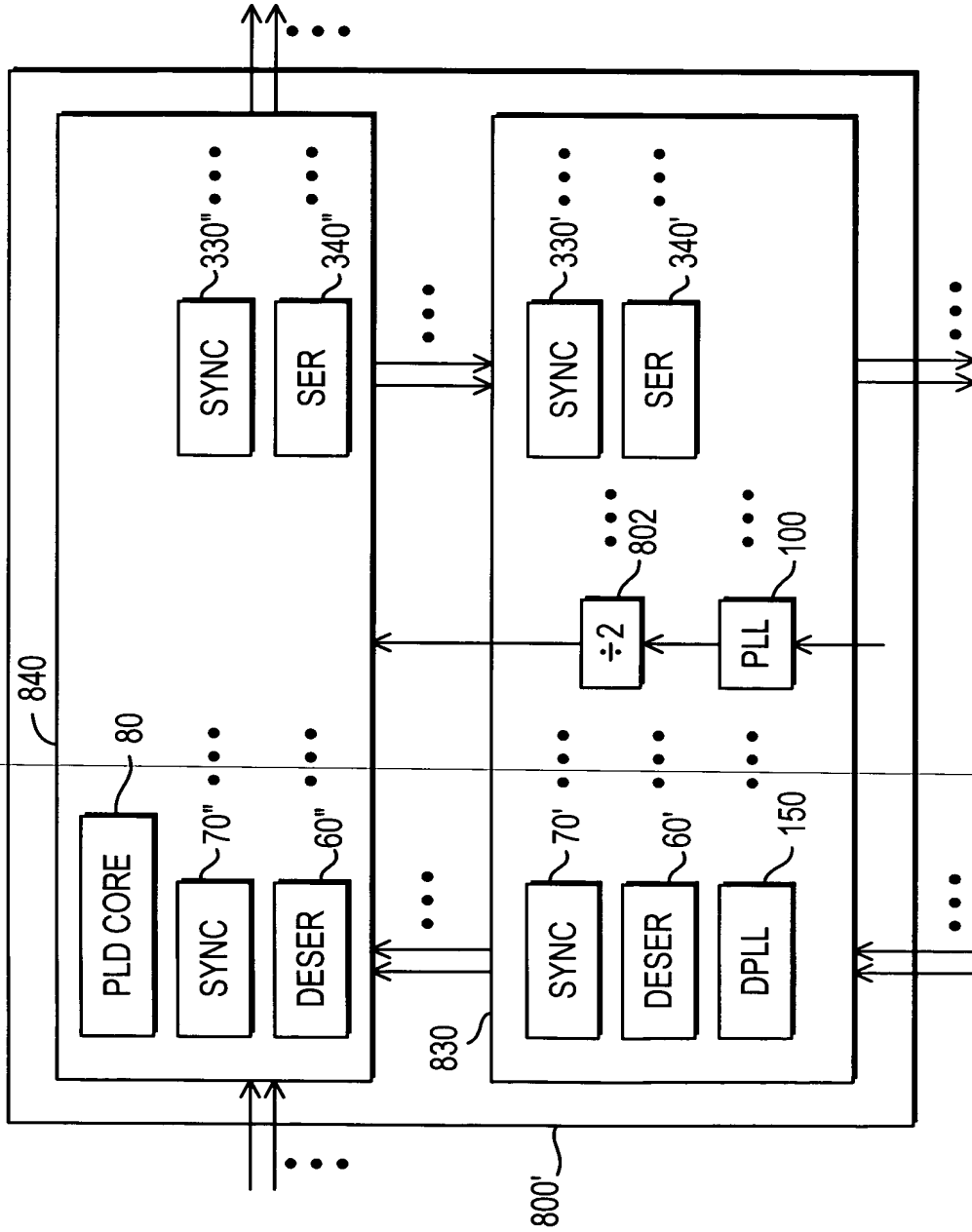


FIG. 11B

FIG. 11B is a block diagram of a system architecture.



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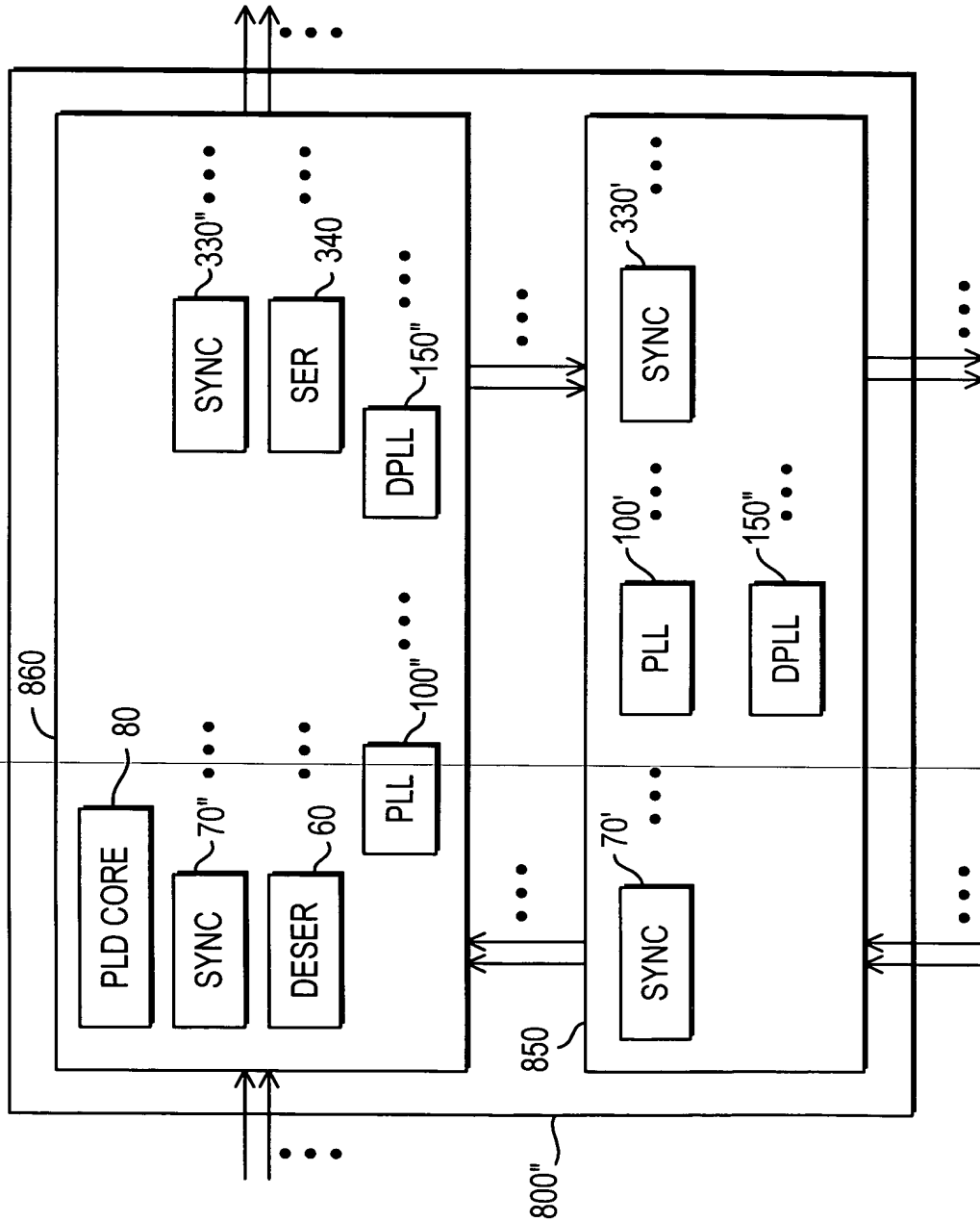


FIG. 11C

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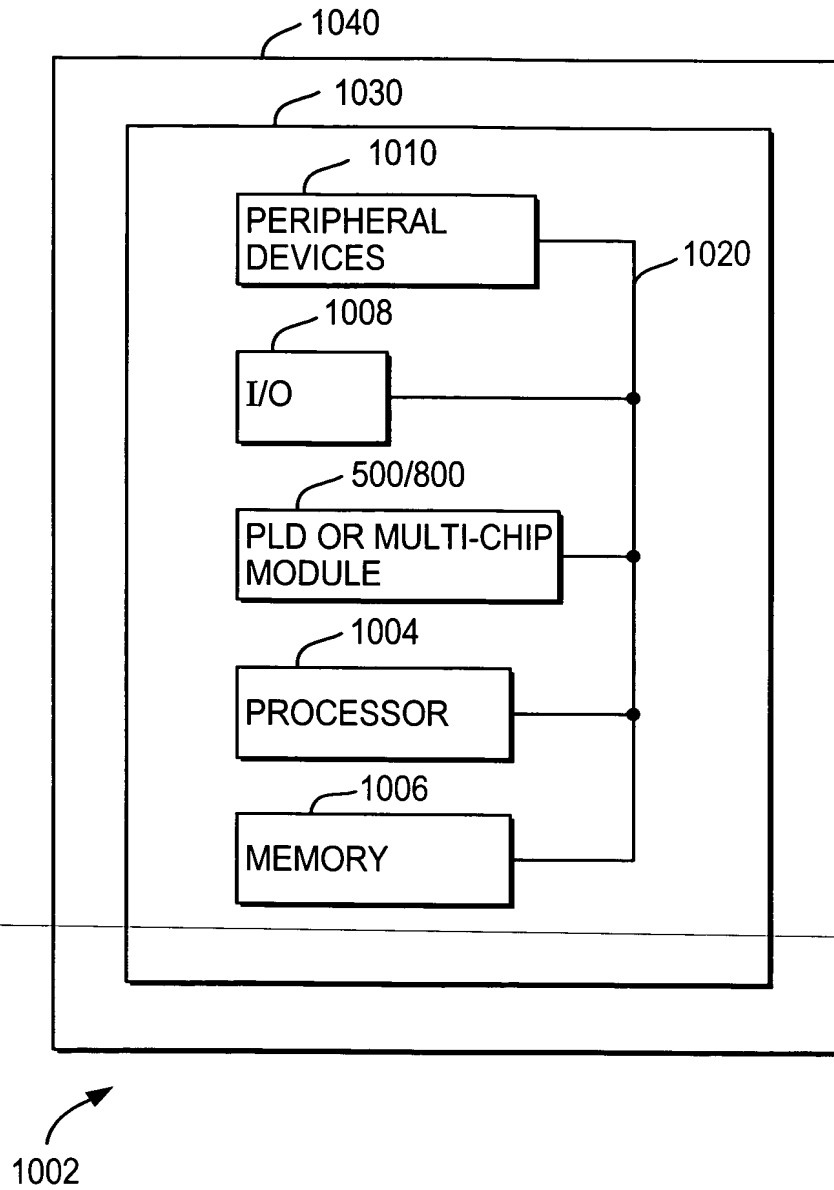
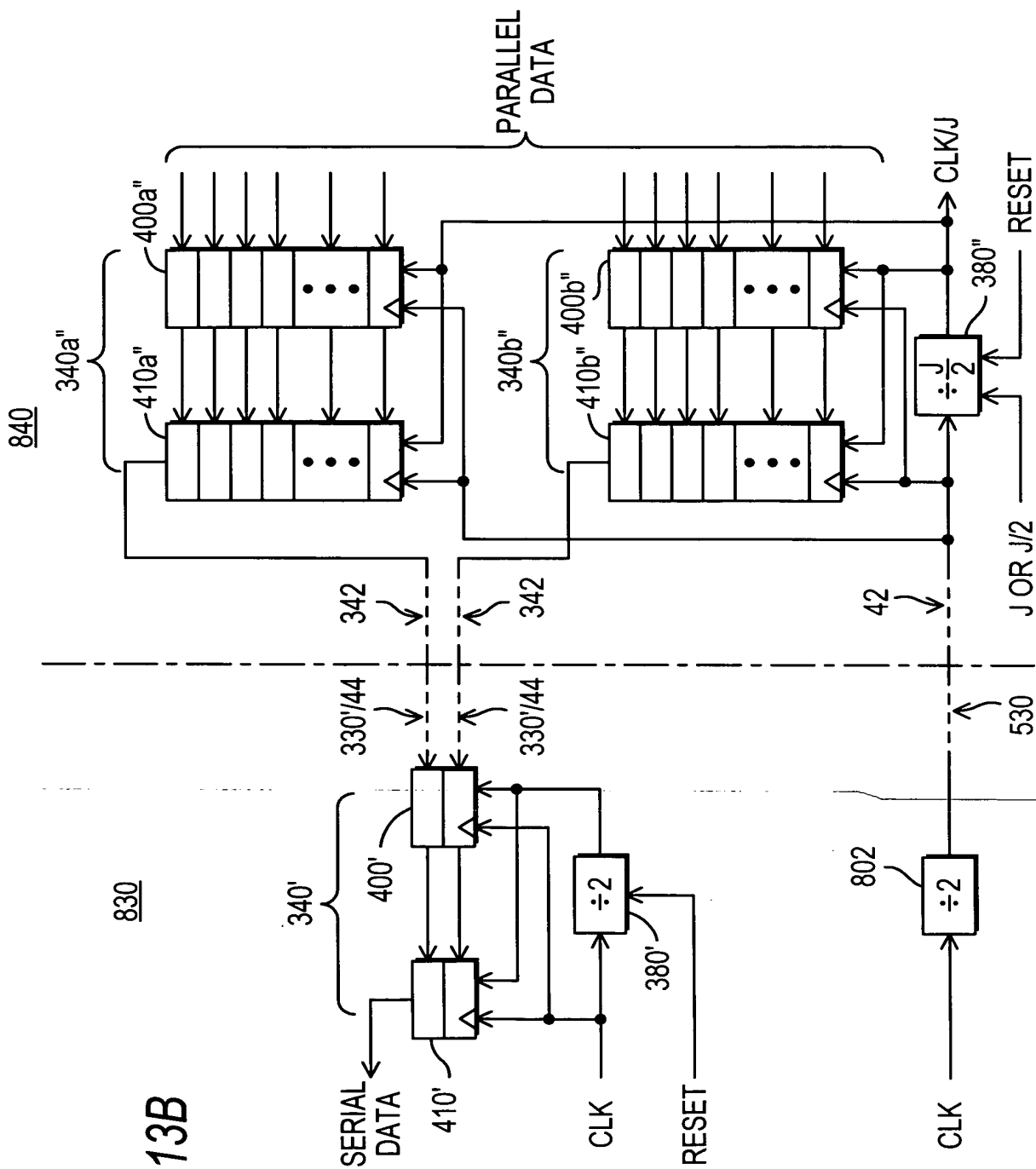


FIG. 12



**FIG. 13B**



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